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PATENT & TRADEMARK OFFICE

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Deshanand Singh et al.	Examiner: Tat, Binh C
Serial No.:	10/679,593	Group Art Unit: 2825
Filed:	October 6, 2003	Docket No.: ALT.P024 (A01002)
Title:	Method and Apparatus for Performing Layout-Driven Optimizations on Field Programmable Gate Arrays	

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Commissioner of Patents
P.O. Box 2144
Alexandria, VA 22313-1450

OK TO ENTER
BT
11/12/07

RESPONSE UNDER 37 C.F.R. 1.116
EXPEDITED PROCEDURE – EXAMINING GROUP 2825

Dear Sir:

In response to the Office Action mailed July 12, 2007, Applicants respectfully request the Examiner to consider the following amendments.